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		Application Number	10/581,076-Conf. #9470		
		Filing Date	May 31, 2006		
		First Named Inventor	Junichi Yamashita		
		Art Unit	2629		
		Examiner Name	D. L. Lewis		
Sheet	1	of	1	Attorney Docket Number	SON-3160

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number Number-Kind Code <sup>2</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
/DL/	BA	JP-54-161288	12-20-1979	Hitachi LTD et al.		
/DL/	BB	JP-05-102312	04-23-1993	NEC Corp		
/DL/	BC	JP-05-259834	10-08-1993	NEC Corp		
/DL/	BD	JP-61-206308	09-12-1986	Seiko Instr & Electronics LTD		
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/DL/	BG	JP-2000-134067	05-12-2000	Isei Denshi Kofun Yugenkoshi		

Examiner Signature	/David Lewis/	Date Considered	03/02/2009
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
/DL/	CA	Razavi, B., "Design of analog CMOS integrated circuits" published, Matuzen Co., Ltd., March 30, 2003	
/DL/	CB	Japanese Action Office issued on August 14, 2008 for corresponding Japanese Application No. 2003-401274	✓

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